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address field address register

addressing modes algorithm

Architecture arithmetic array associative

array associative memory barrel shifter

binary bits block diagram Boolean

Booth's branch instruction buffer bytes

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architecture), designed by J. Hennessy in the early 80's. • To begin with, we shall consider the "integer" section of the architecture only: instructions operate on integer registers (add, sub, etc.), memory (load, store) and PC (branches, unconditional and conditional). 3

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